hierarchical delay tree rake candidate



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| Preferred web (92)  • NDLTD (64)                        |          | delay. The delay searcher searches through a hierarchical delay tree to; identifycorresponds to a candidate delay for, e.g., a RAKE finger in themay include a tree generator andbuilds the hierarchical delay tree  |
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| File types  PDF (211)  HTML (116)  Word (15)            |          | patno: US20050130616delay searcher 164. Delay searcher 164 searchesenergies to identify candidate signal delays for the RAKE receiver 110. Inembodiment, the delay searcher uses a hierarchical tree to facilitate the search for candidate delays. [0035] FIG                 |
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| power control     decoding     clarendon     ieee trans |          | set of correlations are performed over a 25 delay search area, selecting one or more candidates from the first set of correlations based on existence of a multipath component channel tap at a delay and code from the results of the second set                              |
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| oode division     ieee transactions     more ▷          | 4.       | Multipath channel tap delay estimation in a CDMA spread spectrum receiver.  Schelm, Jacob / Shabtay, Ophir / Avital, Yaniv / Reshef, Ehud (Comsys Communication & Signal Processing Ltd.), UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Jun 2006 patno: US7061967 |
|   |          | major components of the rake receiver of FIG. 1 inis shown in FIG. 2. The rake receiver comprises a fingerconstructed using the tap delay estimation mechanism ofmaking the task of the rake receiver more difficultany OVSF code from the tree. The rake receiver functions   |
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